| Notice of Allowability | Application No. | Applicant(s) | Applicant(s) | |
|---|---|---|-----------------------|--|
| | 09/973,002 | HIRAOKA ET AL. | HIRAOKA ET AL. | |
| | Examiner | Art Unit | | |
| | Lourdes Cruz | 2827 | | |
| The MAILING DATE of this communication app | pears on the cover sheet w | ith th correspondenc add | iress | |
| All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT of the Office or upon petition by the applicant. See 37 CFR 1.31 | S (OR REMAINS) CLOSED in 5) or other appropriate comministry. This application is | n this application. If not inclu junication will be mailed in du | ded e course. THIS | |
| This communication is responsive to <u>An RCE filed 7-30-2</u> The allowed claim(s) is/are <u>1,2,4 and 5</u>. The drawings filed on are accepted by the Examinated Acknowledgment is made of a claim for foreign priority. | ner. | or (f). | | |
| a) ☑ All b) ☐ Some* c) ☐ None of the: | | | | |
| 1. ☑ Certified copies of the priority documents have | | | | |
| Certified copies of the priority documents have Copies of the certified copies of the priority description of the priority description. International Bureau (PCT Rule 17.2(a)). * Certified copies not received: | • • | | cation from the | |
| Acknowledgment is made of a claim for domestic priority reference was included in the first sentence of the specifical The translation of the foreign language provisional | cation or in an Application Da | ata Sheet. 37 CFR 1.78. | ce a specific | |
| 6. Acknowledgment is made of a claim for domestic priority in the first sentence of the specification or in an Application | under 35 U.S.C. §§ 120 and | | nce was included | |
| Applicant has THREE MONTHS FROM THE "MAILING DATE" below. Failure to timely comply will result in ABANDONMENT of | | | | |
| 7. A SUBSTITUTE OATH OR DECLARATION must be sub- INFORMAL PATENT APPLICATION (PTO-152) which gi | | | NOTICE OF | |
| CORRECTED DRAWINGS (as "replacement sheets") me (a) ☐ including changes required by the Notice of Draftspe 1) ☐ hereto or 2) ☐ to Paper No | | w (PTO-948) attached | | |
| (b) ☑ including changes required by the proposed drawing(c) ☐ including changes required by the attached Examine | | • | | |
| Identifying indicia such as the application number (see 37 CFR each sheet. Replacement sheet(s) should be labeled as such in | 1.84(c)) should be written on t the margin according to 37 C | the drawings in the front (not the FR 1.121(d). | ne back) of | |
| 9. DEPOSIT OF and/or INFORMATION about the department attached Examiner's comment regarding REQUIREMENT FOR | | | . Note the | |
| Attachment(s) | | | | |
| 1☐ Notice of References Cited (PTO-892) | 5 Notice of Inf | ormal Patent Application (PT | O-152) | |
| 2 Notice of Draftperson's Patent Drawing Review (PTO-948) | | 6☐ Interview Summary (PTO-413), Paper No | | |
| 3 Information Disclosure Statements (PTO-1449 or PTO/SB/08 Paper No | 08), 7□ Examiner's . | 7 Examiner's Amendment/Comment | | |
| 4□ Examiner's Comment Regarding Requirement for Deposit of Biological Material | 8⊠ Examiner's 9⊡ Other | Statement of Reasons for Allo | owance | |
| | L | CARL WHITEHEAD, JR. |)> | |
| | SUP | ERVISORY PATENT EXAMINERS ECHNOLOGY CENTER 2800 | | |

Application/Control Number: 09/973,002

Art Unit: 2827

DETAILED ACTION

Allowable Subject Matter

The following is an examiner's statement of reasons for allowance: There is prior art regarding stacked encapsulated semiconductor devices. See, for example, that Lin et al. (US 6359340) discloses a semiconductor device (see Fig. 10) comprising: A substrate 560 having a first plurality of pads (connected to 512) and a second plurality of pads (connected to 542), a first semiconductor chip 510 mounted on said substrate and having a third plurality of pads 510a; a plate member 520 arranged on said first semiconductor chip having a first end (end under 540) at an inward position from/"of" said first semiconductor chip adjacent to/"from" the third plurality of pads, and a second end being exposed to an outside.

However, the Application as amended specifically recites the combination of a semiconductor device comprising a substrate with first and second pads, first and second chips with pads, a plate member, and the specific structural disposition of the above relative one another, and more specifically the electrical interconnection between chip/plate pads. The claim also recites wherein said plate member has a first portion covered by said first and second semiconductor chips, and a second portion protruding from said first and second semiconductor chips and extending through said seal member to the seal member side surface whereby said plate member is exposed to an outside of said seal member.

Application/Control Number: 09/973,002

Art Unit: 2827

The above is neither obvious over nor anticipated by the prior art of record, which

Page 3

fails to neither alone nor in combination anticipate the present invention as claimed.

Hence the Notice of Allowability included herein.

Any comments considered necessary by applicant must be submitted no later

than the payment of the issue fee and, to avoid processing delays, should preferably

accompany the issue fee. Such submissions should be clearly labeled "Comments on

Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Elle Cruz whose telephone number is (571) 272-1928.

The examiner can normally be reached on M-F 10-6:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Kammand Cuneo can be reached on (571) 272-1957. The fax phone

number for the organization where this application or proceeding is assigned is 703-

308-7722.

Any inquiry of a general nature or relating to the status of this application or

proceeding should be directed to the receptionist whose telephone number is 703-308-

0956.

Elle Cruz

Lourdes Cruz Examiner

Art Unit 2827